AMENDMENTS TO THE SPECIFICATION

Please replace the paragraph beginning on page 1, line 7 with the following amended paragraph:

U.S. Patent Application Serial No. [[_____]] 10/016,518, filed [[_____]] November 1, 2001, titled "WEIGHTED FAIR QUEUE HAVING EXTENDED EFFECTIVE RANGE" (IBM Docket No. ROC920010199US1);

Please replace the paragraph beginning on page 1, line 11 with the following amended paragraph:

U.S. Patent Application Serial No. [[_____]] 10/015,760, filed [[_____]] November 1, 2001, titled "WEIGHTED FAIR QUEUE HAVING ADJUSTABLE SCALING FACTOR" (IBM Docket No. ROC920010201US1);

Please replace the paragraph beginning on page 1, line 15 with the following amended paragraph:

U.S. Patent Application Serial No. [[_____]] 10/002,085, filed [[_____]] November 1, 2001, titled "EMPTY INDICATORS FOR WEIGHTED FAIR QUEUES" (IBM Docket No. ROC920010202US1);

Please replace the paragraph beginning on page 1, line 19 with the following amended paragraph:

U.S. Patent Application Serial No. [[_____]], filed [[_____]], titled "QoS SCHEDULER AND METHOD FOR IMPLEMENTING PEAK SERVICE DISTANCE USING NEXT PEAK SERVICE TIME VIOLATED INDICATION" (IBM Docket No. ROC920010203US1) now U.S. Patent No. 6,973,036 issued on December 6, 2005;

Please replace the paragraph beginning on page 1, line 25 with the following amended paragraph:

U.S. Patent Application Serial No. [[_____]], filed [[_____]], titled "QoS SCHEDULER AND METHOD FOR IMPLEMENTING QUALITY OF SERVICE WITH AGING STAMPS" (IBM Docket No. ROC920010204US1);

Please replace the paragraph beginning on page 1, line 30 with the following amended paragraph:

U.S. Patent Application Serial No. [[_____]], filed
[[_____]], titled "QoS SCHEDULER AND METHOD FOR
IMPLEMENTING QUALITY OF SERVICE WITH CACHED STATUS ARRAY"
(IBM Docket No. ROC920010205US1) now U.S. Patent No.
7,046,676 issued on May 16, 2006; and

Please replace the paragraph beginning on page 2, line 4 with the following amended paragraph:

U.S. Patent Application Serial No. [[_____]], filed
[[_____]], titled "QoS SCHEDULER AND METHOD FOR
IMPLEMENTING QUALITY OF SERVICE ANTICIPATING THE END OF A
CHAIN OF FLOWS" (IBM Docket No. ROC920010206US1) now U.S.
Patent No. 6,982,986 issued on January 3, 2006.

Please replace the paragraph beginning on page 14, line 21 with the following amended paragraph:

        FIG. 4 also illustrates a number of other
features of the inventive scheduling queues 42, including
the use of a conventional current pointers 52. With
respect to each scheduling queue 42, the current pointer 52
indicates the slot most recently serviced or currently
being serviced for the respective scheduling queue. Also
shown at 54 in FIG. 4 are indicators that indicate whether
the respective scheduling queues are empty. In at least
one embodiment, the indicators 54 are provided in
accordance with an invention disclosed in co-pending patent
application Serial No. [[_____]] 10/002,085, filed
[[_____]] November 1, 2001 (Attorney Docket No.
ROC920010202US1). The entire disclosure of this co-pending
patent application is incorporated herein by reference.
Other indication schemes may be used to indicate whether
each scheduling queue 42 is empty. It is also indicated at
56 in FIG. 4 that a round robin process may search each of
the 64 scheduling queues 42 in turn. Other search
processes may be employed.

Please replace the paragraph beginning on page 22, line 1 with the following amended paragraph:

Still further, it is contemplated to implement the present invention in connection with scheduling queues having extended ranges. Such scheduling queues may include subqueues having different respective ranges and resolutions, as disclosed in co-pending patent application Serial No. [[_____]] 10/016,518, filed [[_____]] November 11, 2001 (Attorney Docket No. ROC920010199US1). This co-pending patent application is incorporated herein by reference.